

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L10	1	(integrated circuit AND processing core AND internal memory AND test routines AND production test AND in-product test AND system-level test AND interface AND control signal AND first output signal AND test result). clm.	US-PGPUB; USPAT; USOCR	ADJ	ON	2008/04/11 14:22

4/ 11/ 08 2:25:18 PM

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